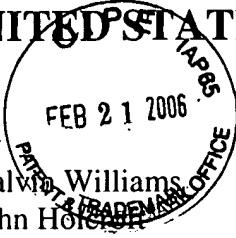


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Kalvin Williams
John Holsen
Christopher Lane



Serial No. : 09/915,833
Filed : July 26, 2001
For : Method for Multi-Processor
Communication Within A Shared
Memory Architecture

Group Art Unit : 2126
Examiner : Courtenay III, St. John
Atty Docket : 1496.00136 / 01-213

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

2/21/06
Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 2/21/2006

Respectfully submitted,

Henry Groth

Henry Groth

Reg. No. 39,696